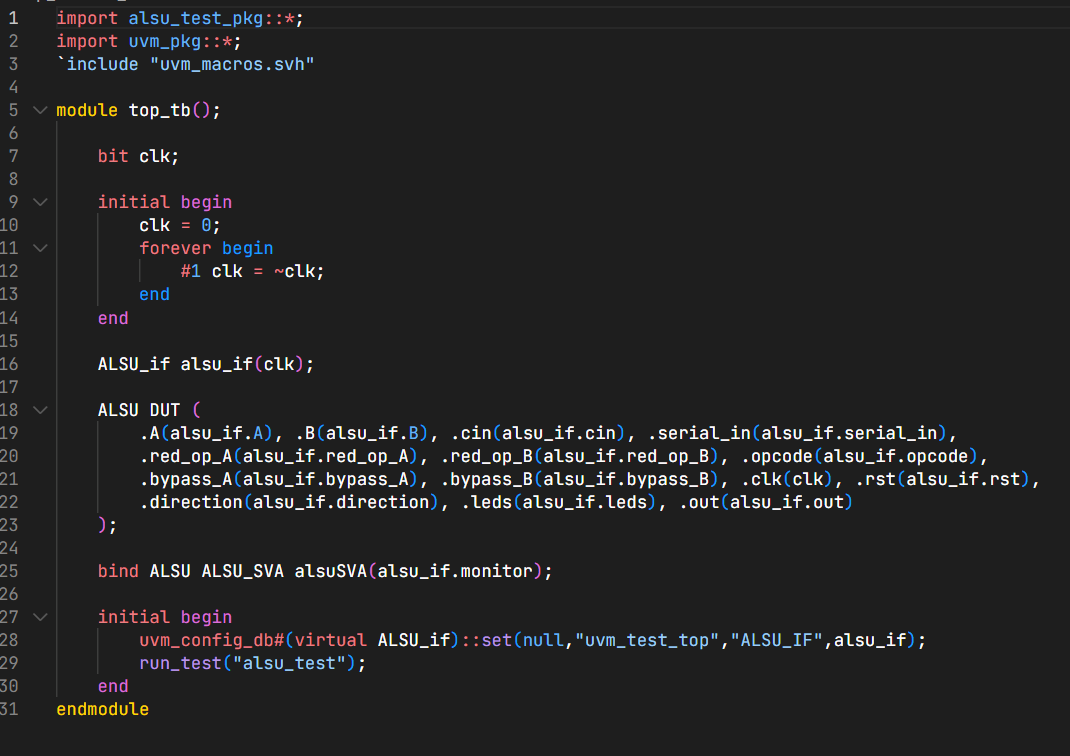
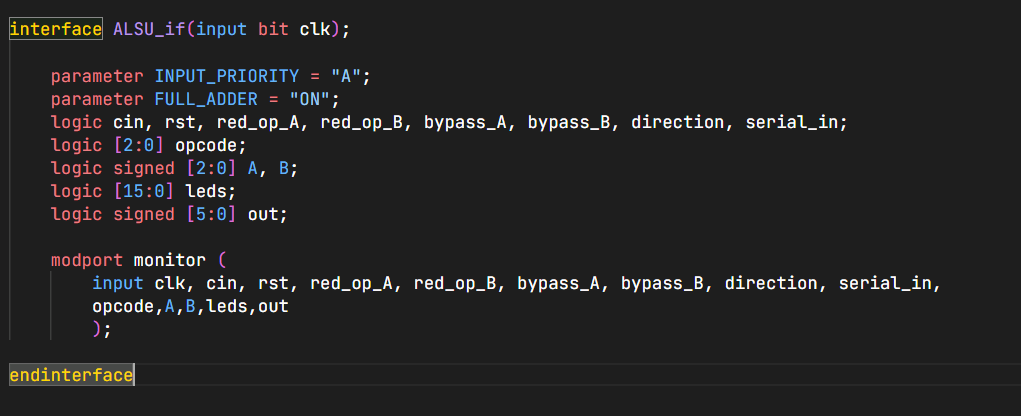
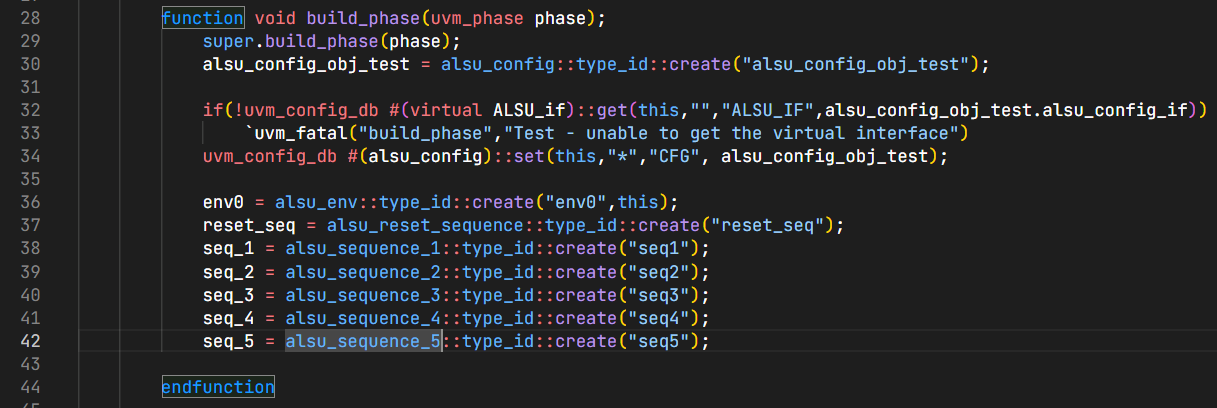
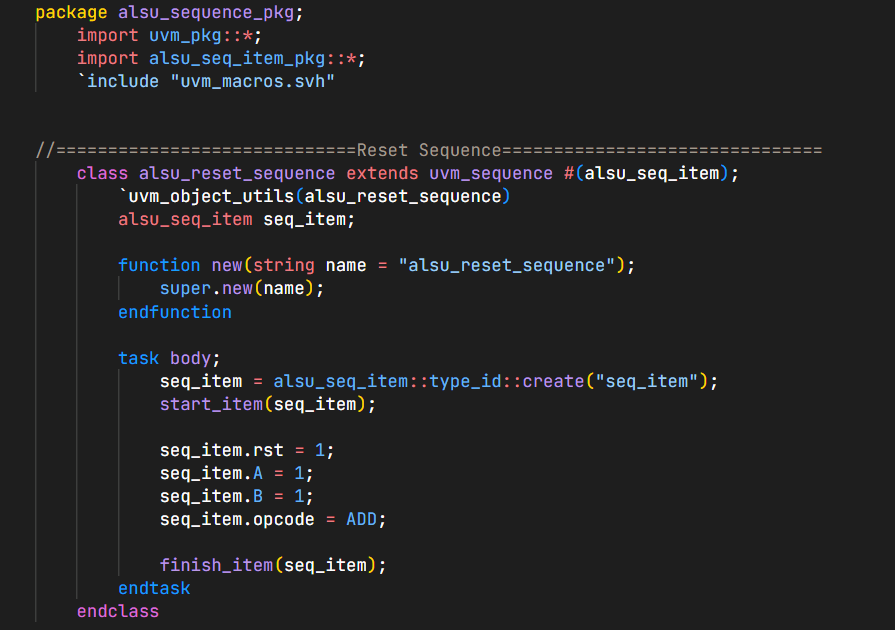
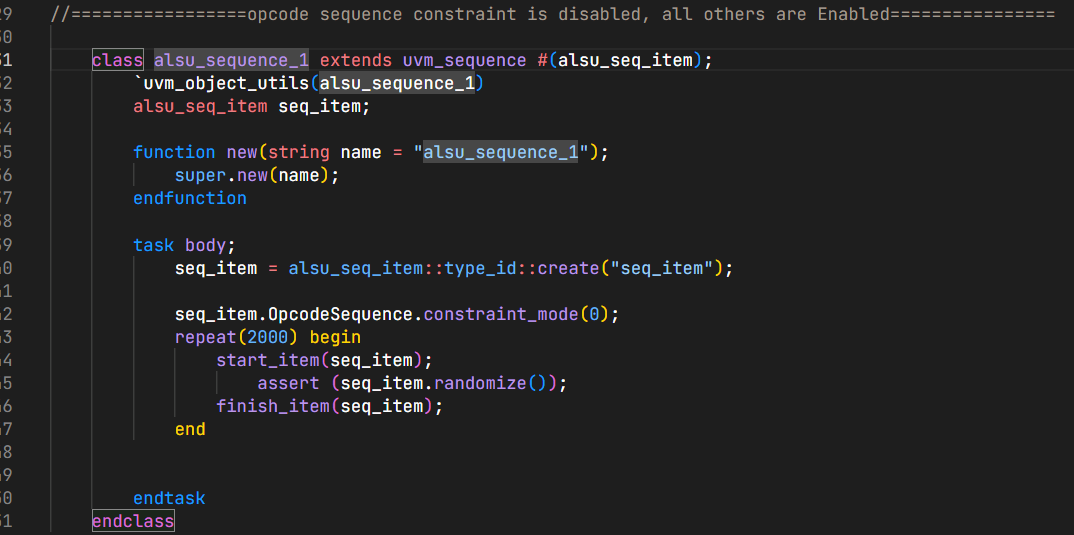
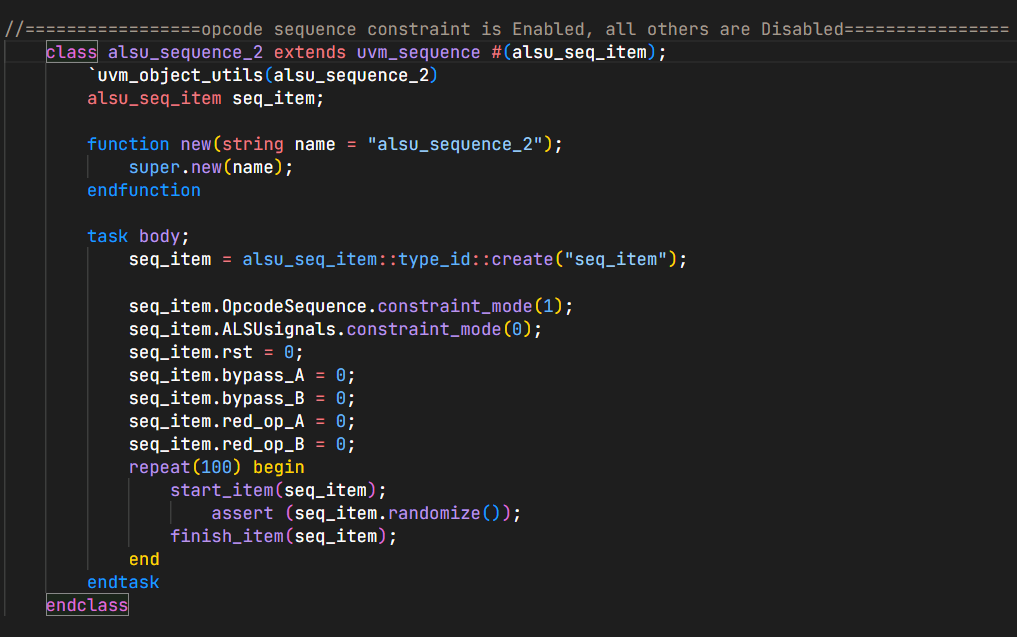
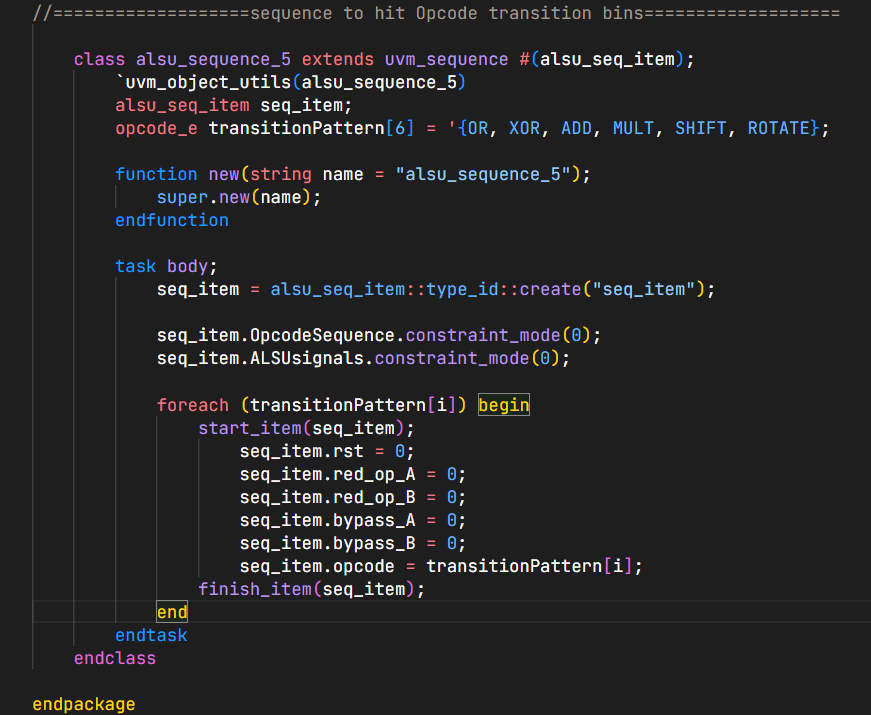
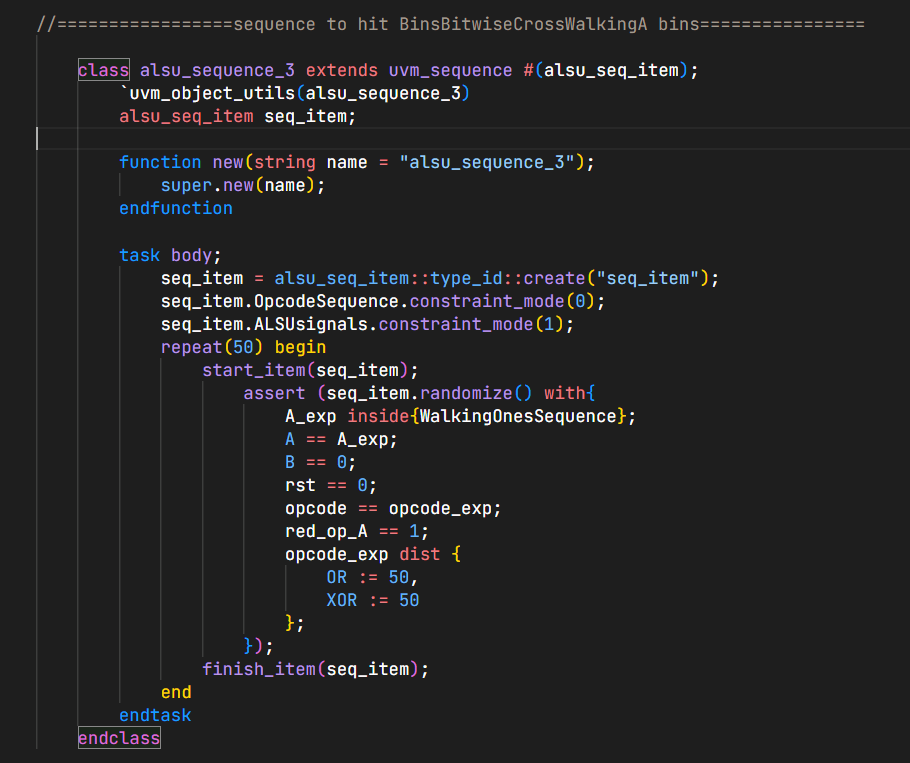
# Digital Verification using SV and UVM

# Assignment-6

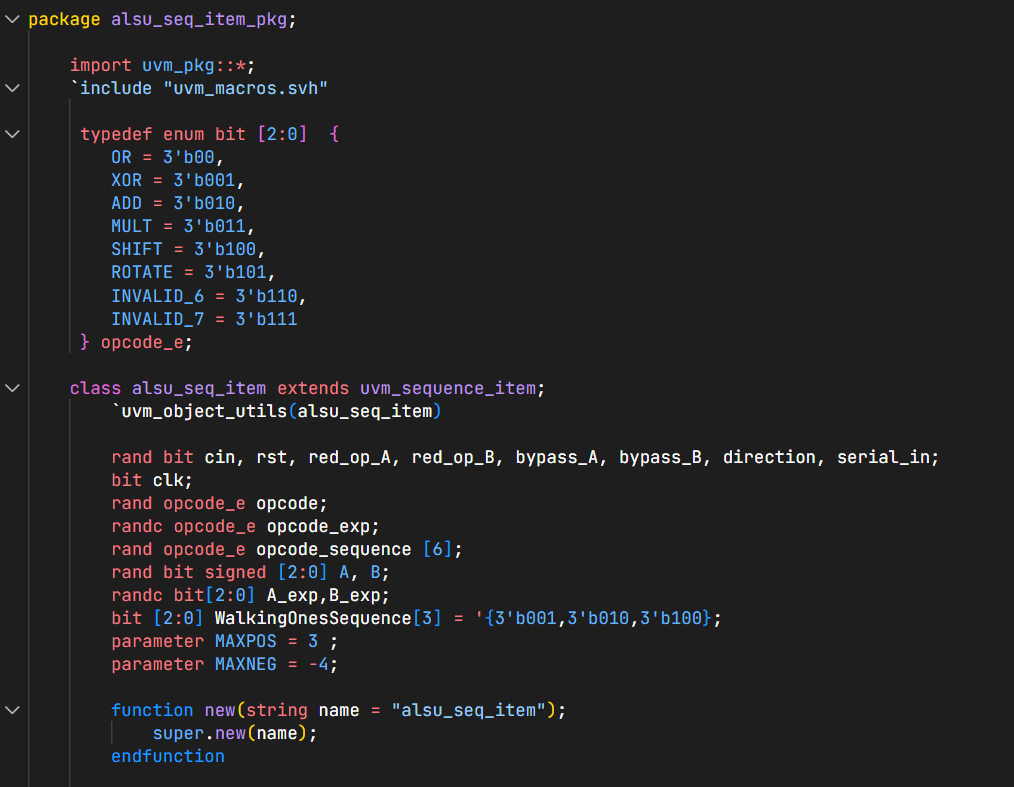
# Name: Fares Khalaf Sultan

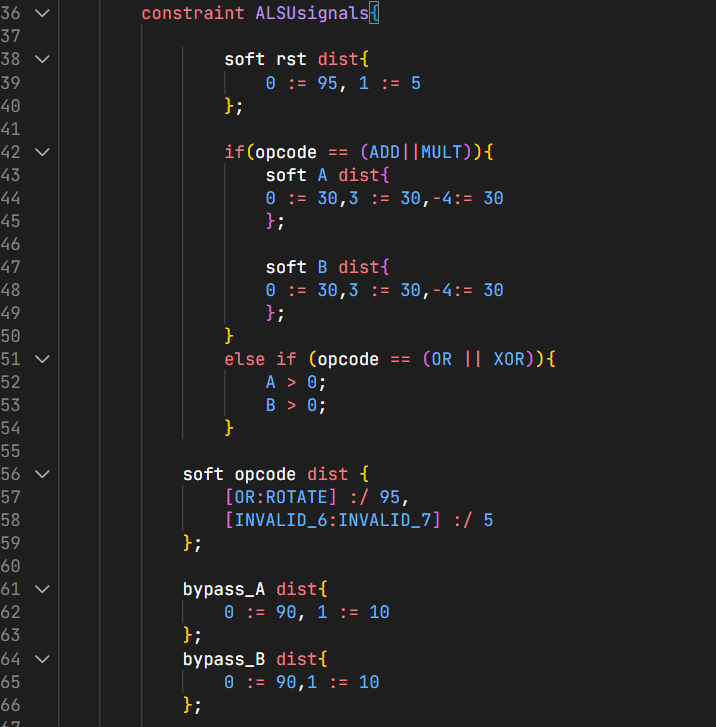
* **Top module:**
* **Interface:**
* **Test:**
* **Sequences:**



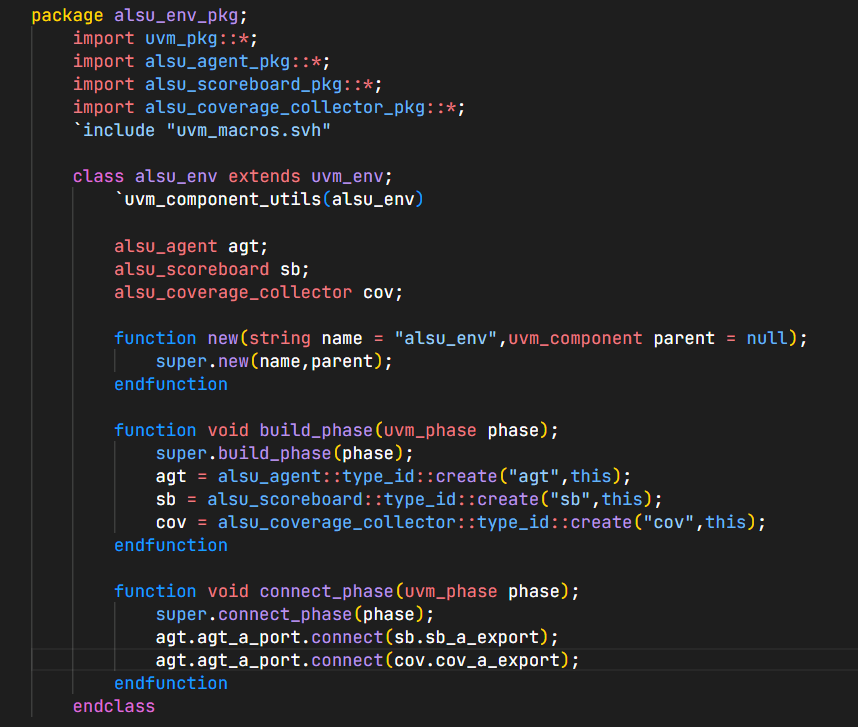
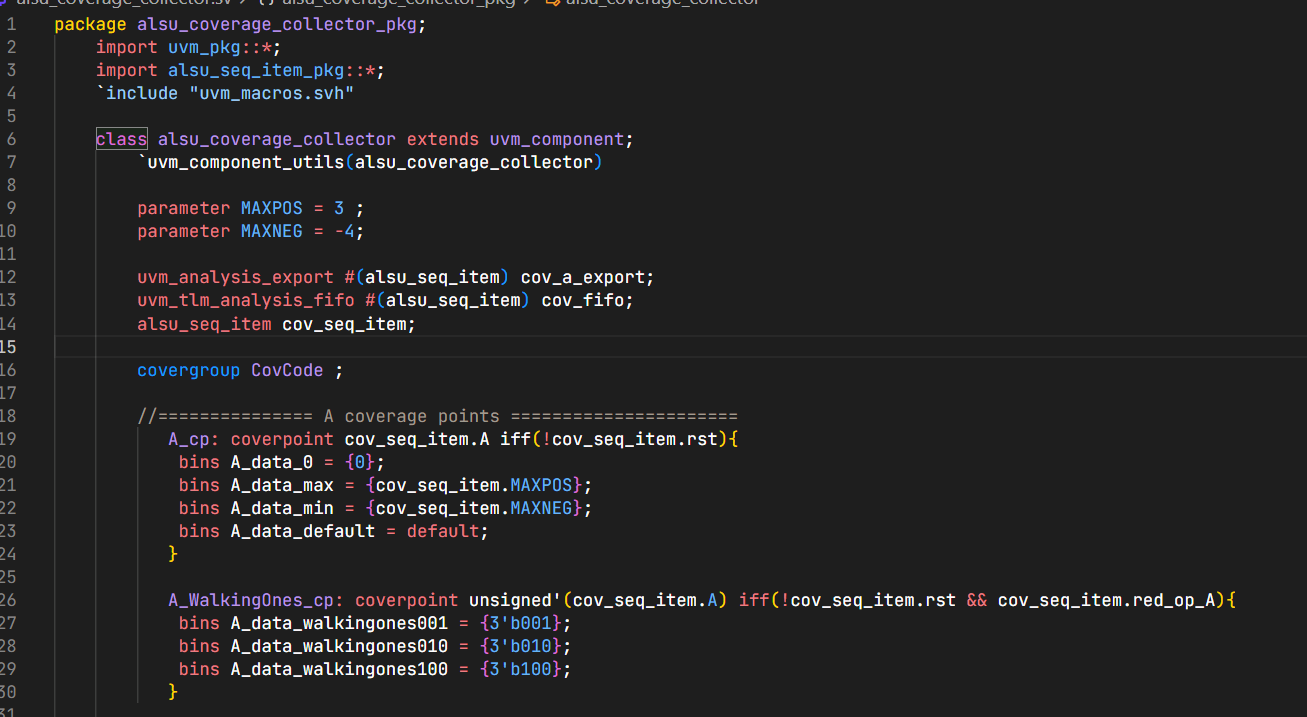


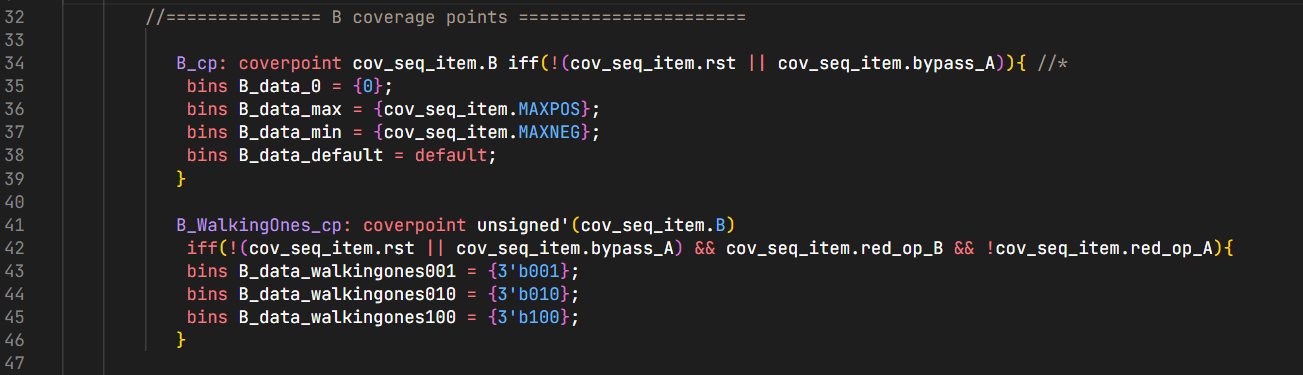
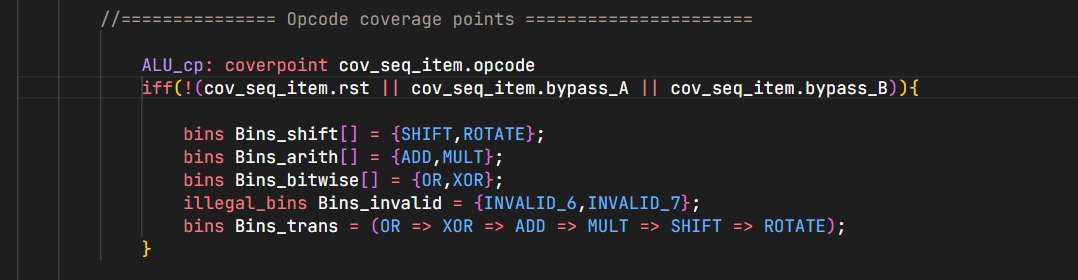
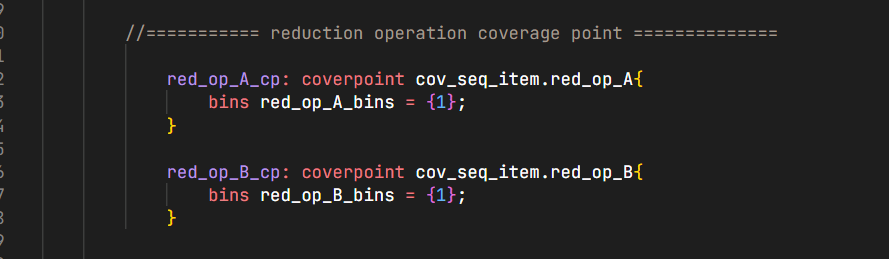
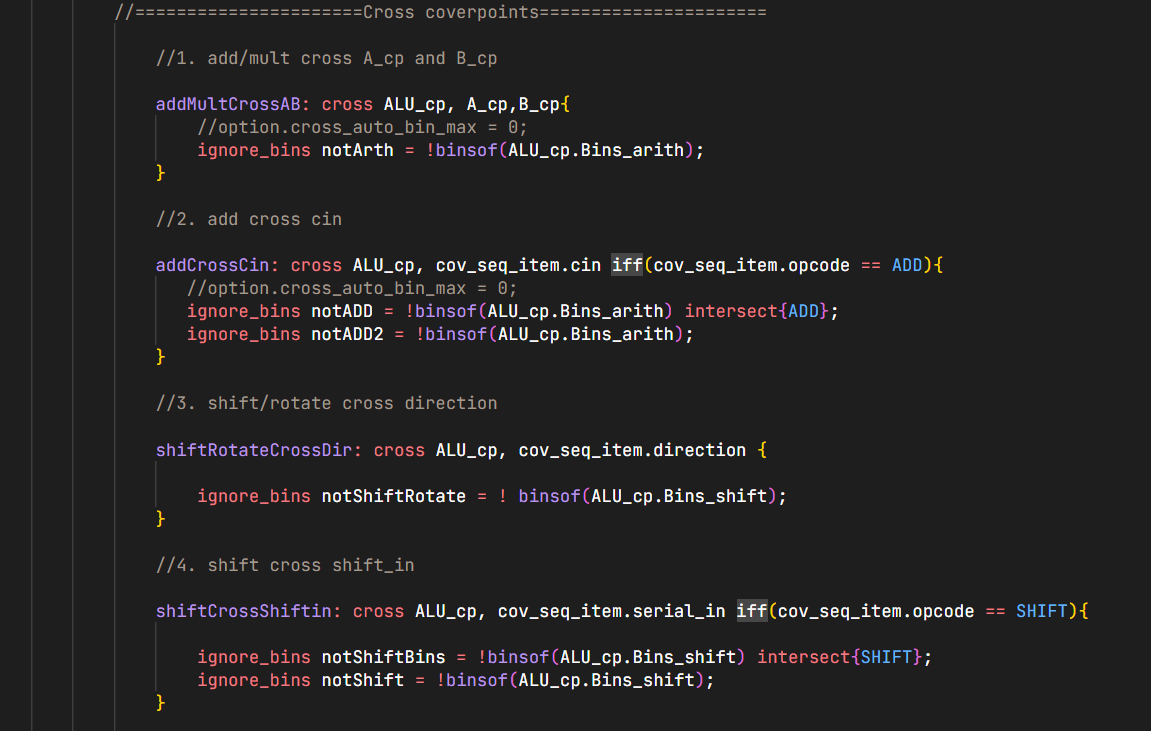
* **Sequence item:**

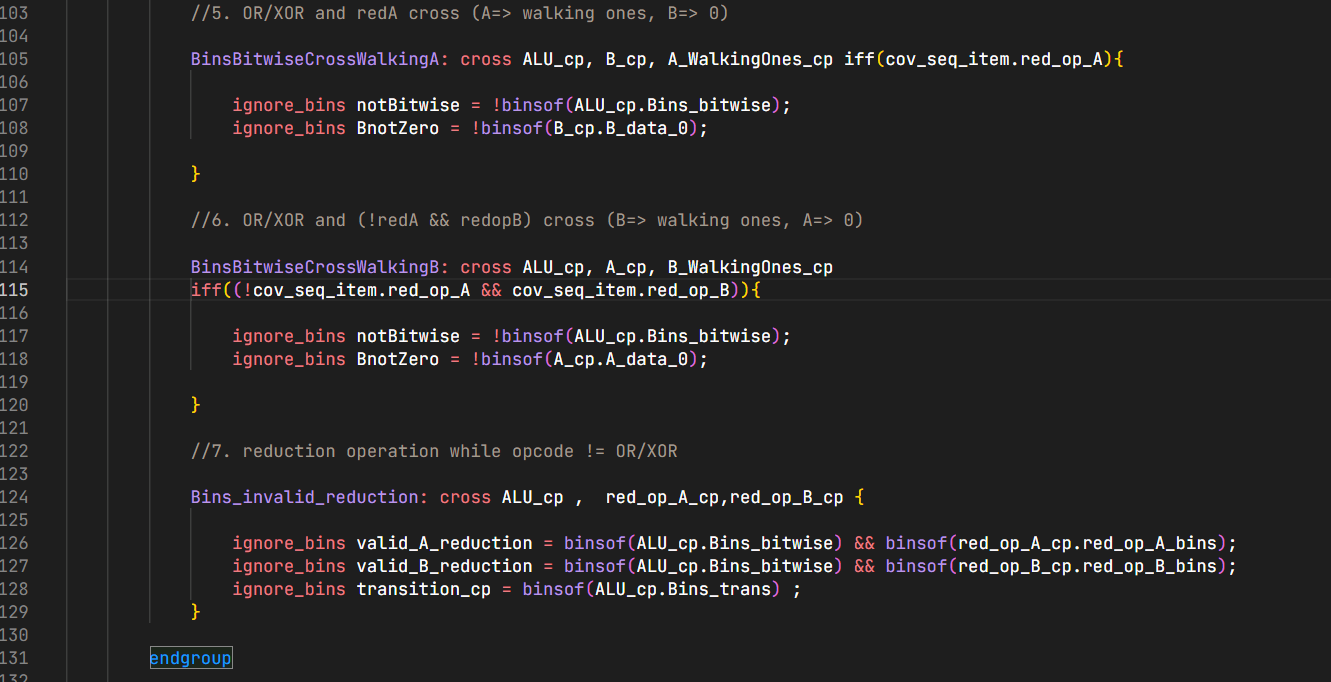
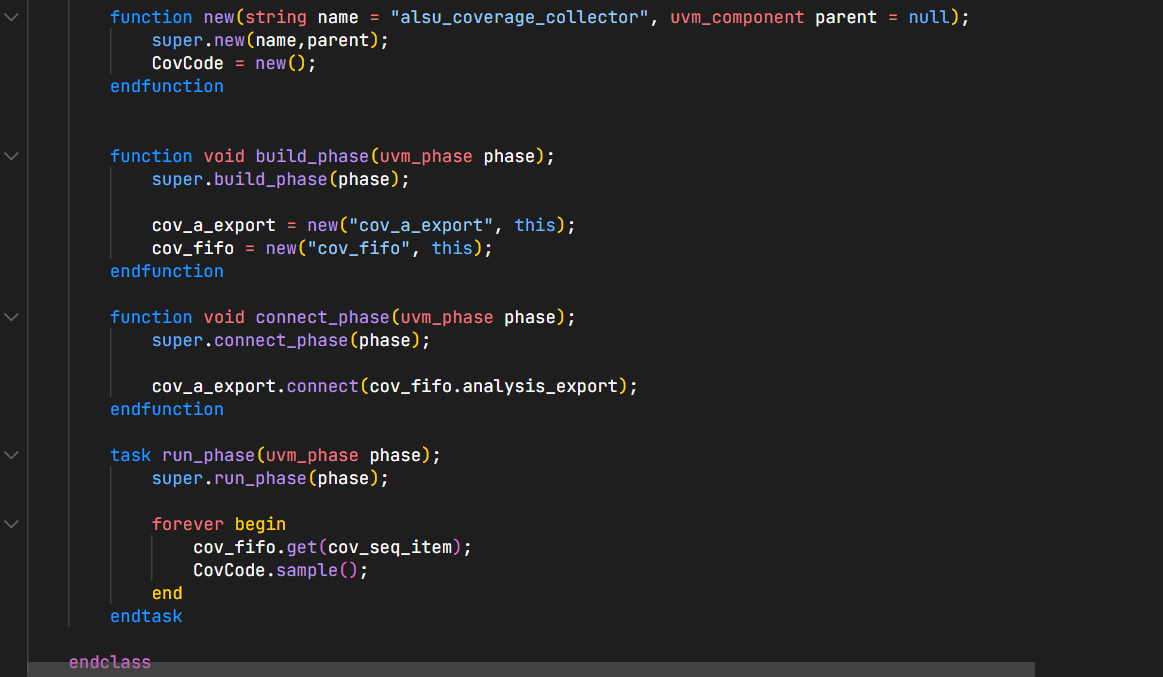


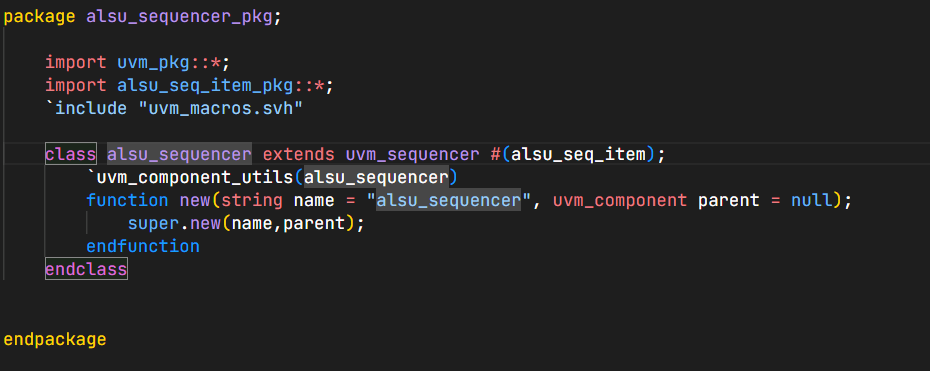
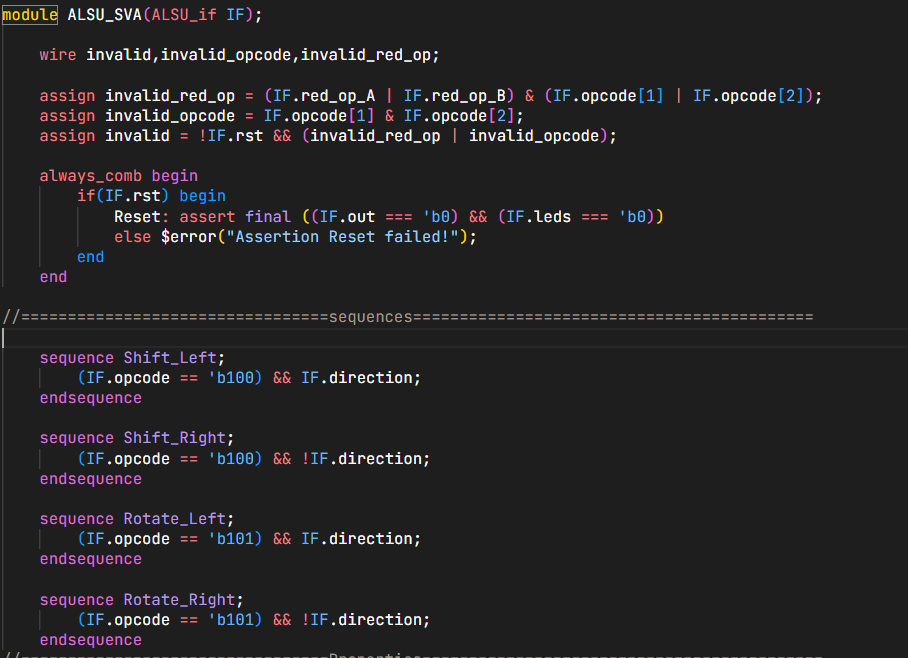
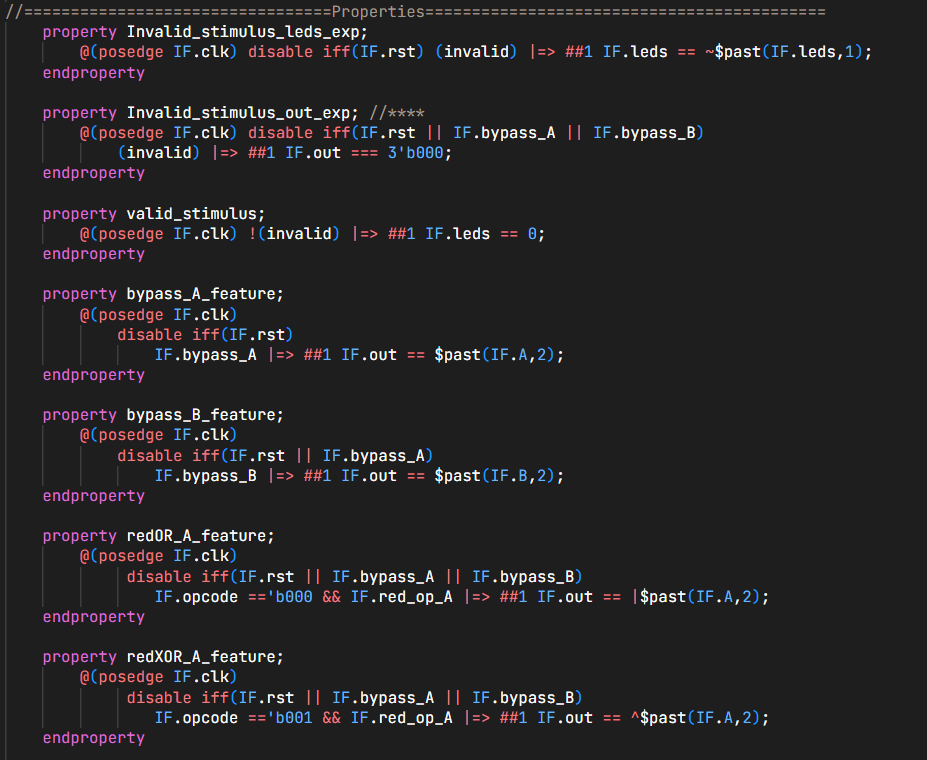


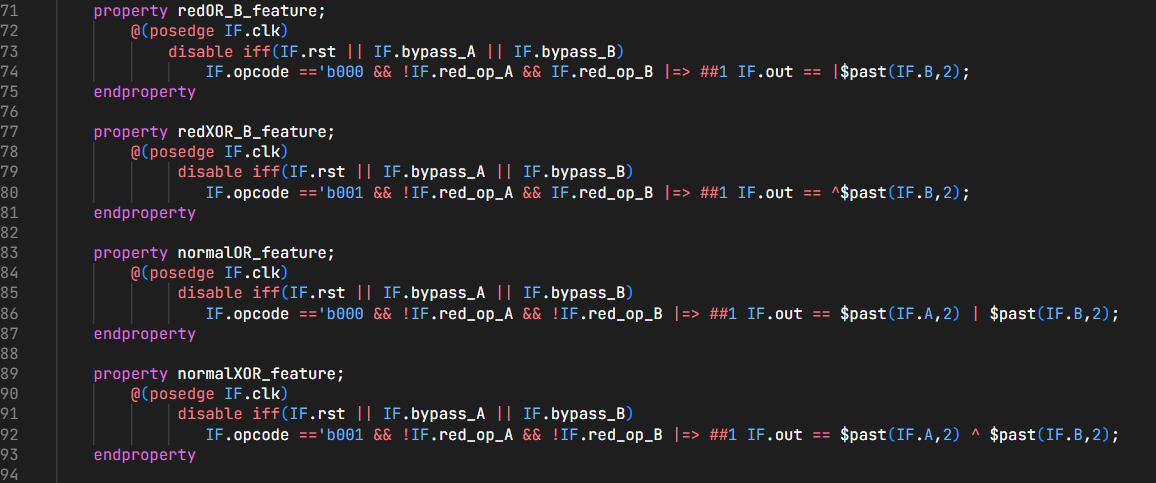
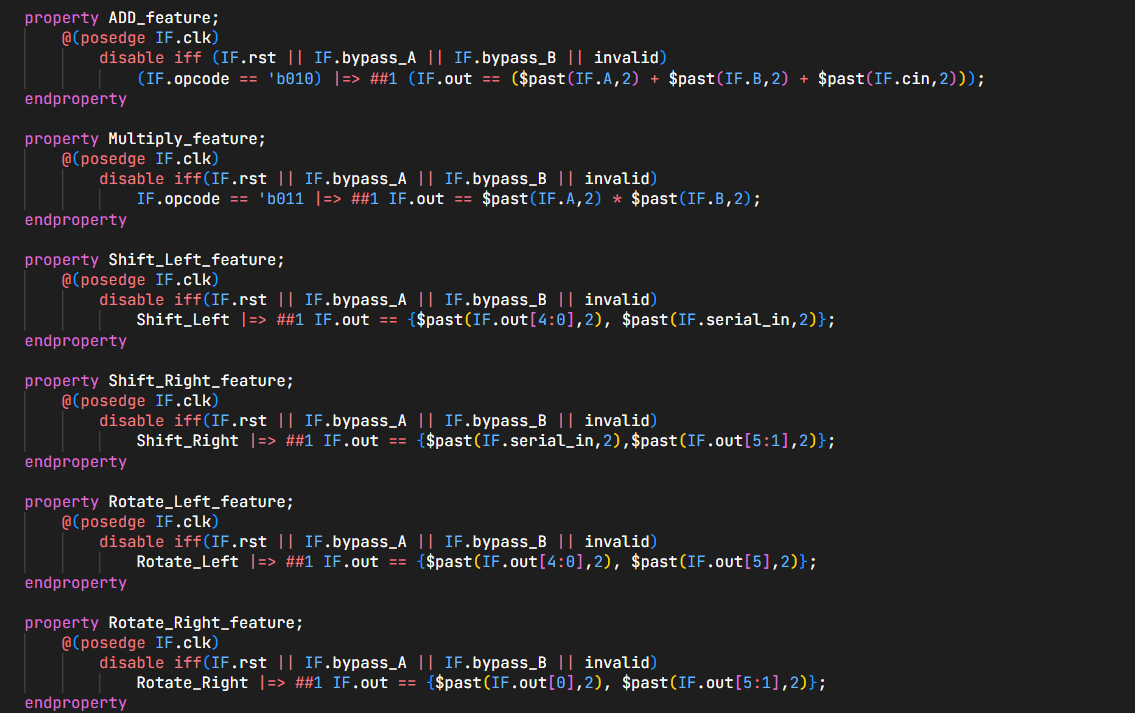
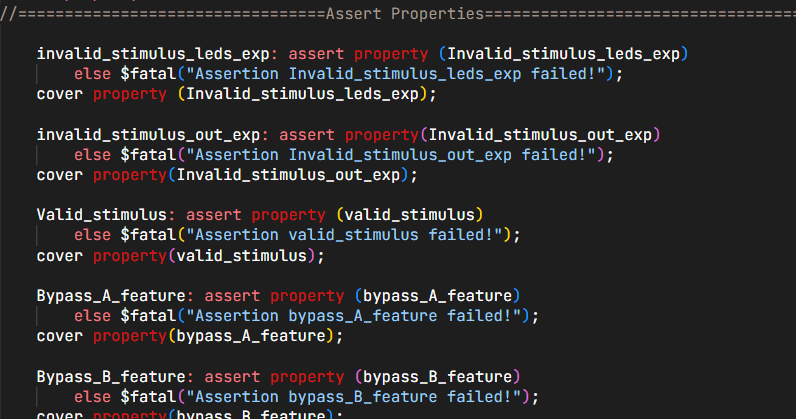


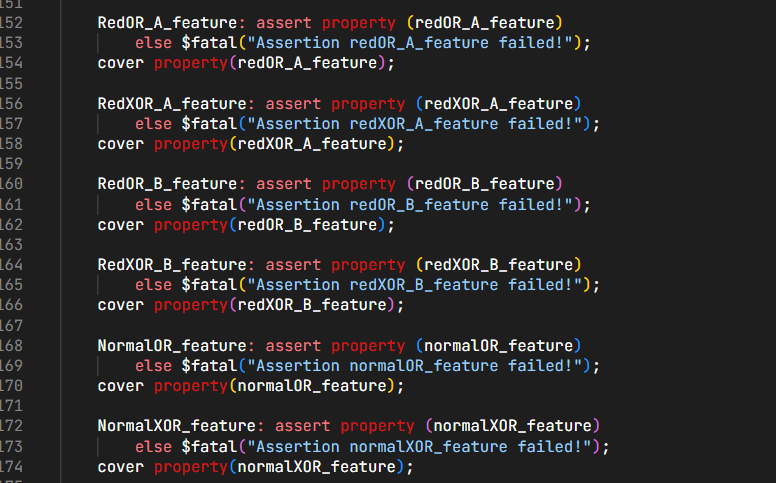
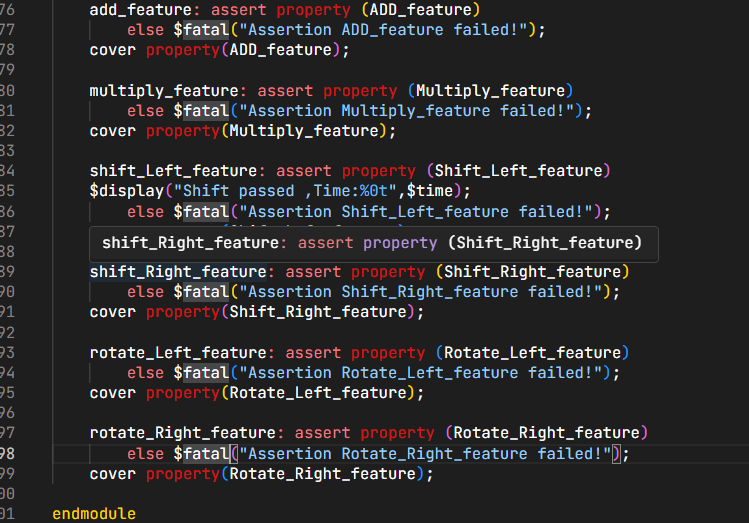
* **Environment:**
* **Scoreboard:**
* **Coverage collector:**



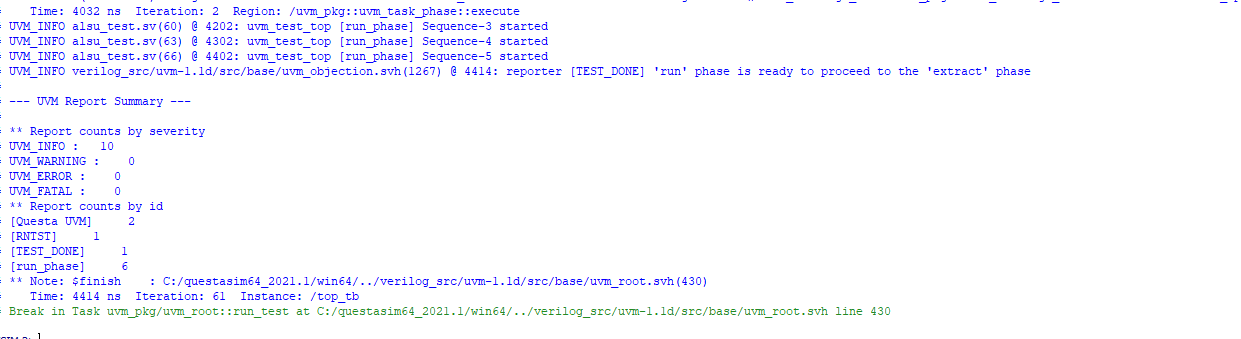


* **Agent:**
* **Sequencer:**
* **Driver:**
* **Monitor:**
* **Assertions module:**



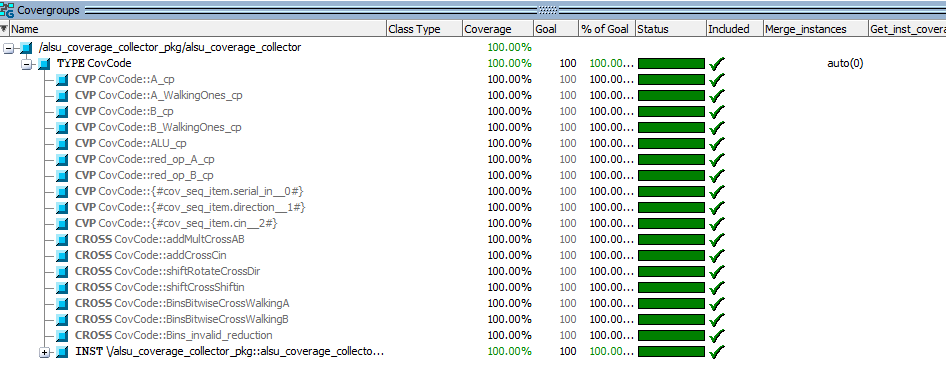


* **Results:**





* **Functional coverage:**



* **Assertion coverage:**

